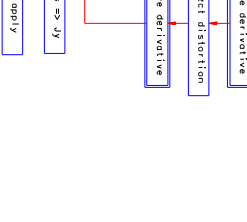
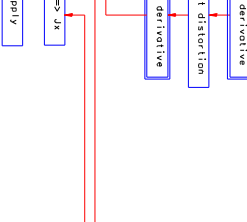
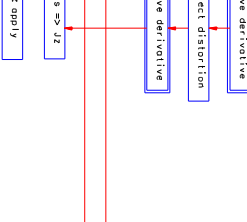
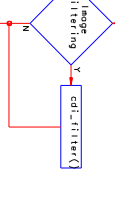
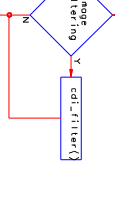
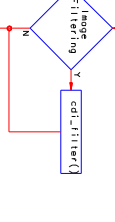
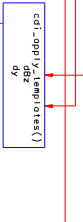
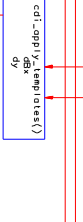
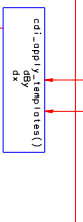
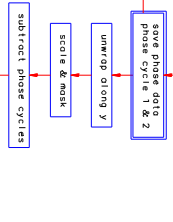
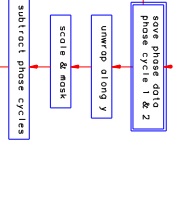
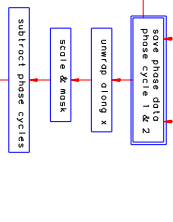
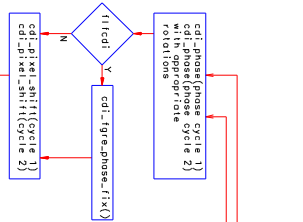
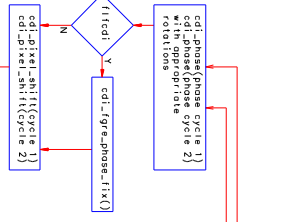
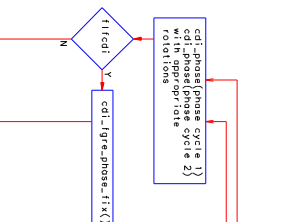
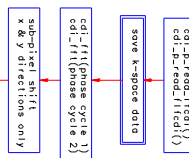
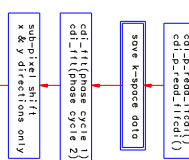
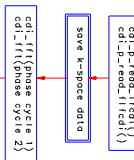


CDI Processor 3.21 Block Diagram

By acquisition

Bx acquisition

Bz acquisition



Uz

Ux

Uy